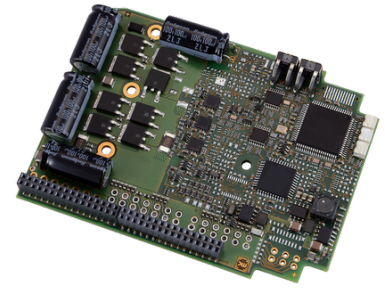


Servo amplifier

mcDSA-FS40-M

Article number: 1516009



Picture similar

Technical data

Supply voltages	
Electronic supply voltage U_e^{*1}	9..30 V
Electronic current consumption @ $U_e=24V^{*2}$	typ. 35 mA
Power supply voltage U_p^{*3}	9..60 V
Output current	
Max. output current	20 A
Continuous output current @ $U_p=24V^{*4}$	7 A
Continuous output current @ $U_p=48V^{*4}$	6 A
PWM	
Output voltage	85% U_p
PWM frequency	25, 32, 50 ^{*5} kHz
Commutation type	Field Oriented Control
Mechanical	
Size LxWxH	97 x 71 x 12 mm
Weight	55 g
Environment	
Protection class	IP00
Ambient temperature (operation)	-25..70 °C
Ambient temperature (storage)	-25..85 °C
Rel. humidity (non-condensing)	5..90 %
CAN bus	
Protocol	DS301
Device profile	DS402
Max. baudrate	1 Mbit/s
CAN specification	2.0B
Galvanically isolated	no

Digital inputs	
Number	4 (Din0..3)
Low voltage	-30..5 V
High voltage	6..30 V
Analog inputs	
Number	2 (Ain0..1)
Signal type - Ain	0..10 V, 12 Bit, single ended

*1 No reverse polarity protection, the destruction limit is at overvoltage of $\geq 33V$ or short-term peak voltage of $37V < 1s$

*2 power amplifier switched off, 5V output (sensor supply) is free

*3 No reverse polarity protection, the destruction limit is at overvoltage of $\geq 80V$

*4 connector cable with max. possible cable cross-section, PWM frequency 32 kHz, ambient temperature 40 °C ($t > 40$ °C derating), RMS current: 7 A \rightarrow 5.7 Aeff, 6 A \rightarrow 4.9 Aeff

no guarantee, since value is determined empirical, please consider the application notes to determine the continuous current

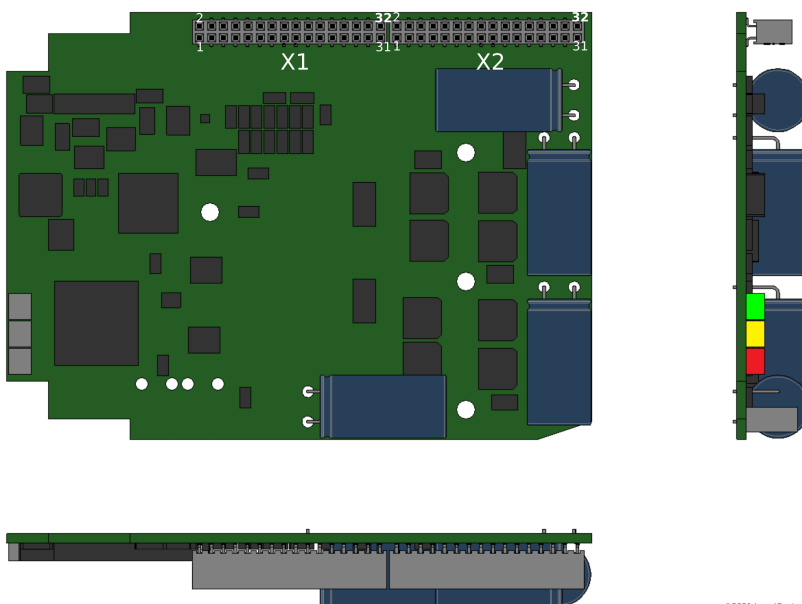
*5 default value

Additional technical data are available in mcManual.



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Scheme



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Terminal assignment

X1	I/O's and CAN	
1	CAN Hi	CAN High
2	CAN Lo	CAN Low
3	res.	Reserved
4	res.	Reserved
5	res.	Reserved
6	res.	Reserved
7	Din2	Digital input 2
8	Din3	Digital input 3
9	Din0	Digital input 0
10	Din1	Digital input 1
11	Ain0	Analog input 0
12	Ain1	Analog input 1
13	SpiMISO	mcSPI Master In
14	Spi/SS	mcSPI Slave Select
15	SpiMOSI	mcSPI Master Out
16	SpiCLK	mcSPI Clock
17	Rx0	UART0 Receive Signal
18	Tx0	UART0 Transmit Signal
19	Erw1	mcSPI expansion signal 1
20	Erw2	mcSPI expansion signal 2
21	res.	Reserved
22	res.	Reserved
23	res.	Reserved
24	res.	Reserved
25	res.	Reserved
26	res.	Reserved
27	res.	Reserved
28	res.	Reserved
29	res.	Reserved
30	res.	Reserved
31	res.	Reserved
32	res.	Reserved

X2	Motor	
1	+U5V	5V auxiliary voltage
2	GND	Ground for 5V auxiliary voltage
3	res.	Reserved
4	res.	Reserved
5	+Ue24V	Electronic supply voltage
6	+Ue24V	Electronic supply voltage
7	Md	Motor phase D
8	Md	Motor phase D
9	Md	Motor phase D
10	Md	Motor phase D
11	Mc	Motor phase C
12	Mc	Motor phase C
13	Mc	Motor phase C
14	Mc	Motor phase C
15	Mb	Motor phase B
16	Mb	Motor phase B
17	Mb	Motor phase B
18	Mb	Motor phase B
19	Ma	Motor phase A
20	Ma	Motor phase A
21	Ma	Motor phase A
22	Ma	Motor phase A
23	GND	Ground for power and electronic supply voltage
24	GND	Ground for power and electronic supply voltage
25	GND	Ground for power and electronic supply voltage
26	GND	Ground for power and electronic supply voltage
27	+Up	Power supply voltage
28	+Up	Power supply voltage
29	+Up	Power supply voltage
30	+Up	Power supply voltage
31	FE	Functional earth
32	FE	Functional earth